Synchronous Diffusion Programmable ROM Compiler – FS90A_B_SD

Key Features
- Operating voltage range: 2.25V ~ 2.75V
- Operating junction temp. range: 0°C ~ 115°C
- Recommended operating ambient temp. range: 0°C ~ 85°C
- Minimum metal requirement: 4 metal layers
- Synchronous read operation
- One (1) read address port
- Diffusion programmable
- Fully customized layout density
- Different aspect ratios to best fit chip floorplan
- Variable capacity: maximum 1M bits
- Tri-state output buffer

General Description
The FS90A_B_SD is a diffusion programmable, synchronous ROM. It is implemented according to UMC’s 0.25µm technology. Different combinations of words, bits, and aspect ratios can be used to generate the most desirable configurations.

By requesting for the desired size and timing constraints, the FS90A_B_SD compiler is capable of providing suitable synchronous ROM layout instances in minutes. It automatically generates data sheets, Verilog behavioral simulation models, SCS or Viewlogic symbols, place & route models and test patterns for use in ASIC designs.

Quick Reference

<table>
<thead>
<tr>
<th></th>
<th>1M bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum Capacity</td>
<td></td>
</tr>
<tr>
<td>Maximum Words</td>
<td>64K</td>
</tr>
<tr>
<td>Minimum Words</td>
<td>128</td>
</tr>
<tr>
<td>Maximum Bits</td>
<td>128</td>
</tr>
<tr>
<td>Minimum Bits</td>
<td>1</td>
</tr>
<tr>
<td>Bit Increment</td>
<td>1</td>
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<tr>
<td>Aspect Ratio (Block Mux)</td>
<td>1, 2, 4, 8</td>
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</tbody>
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Symbol

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